

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 07-287548

(43)Date of publication of application : 31.10.1995

(51)Int.Cl. G09G 3/28
H01J 11/00
H04N 5/66

(21)Application number : 06-079077

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(22)Date of filing : 18.04.1994

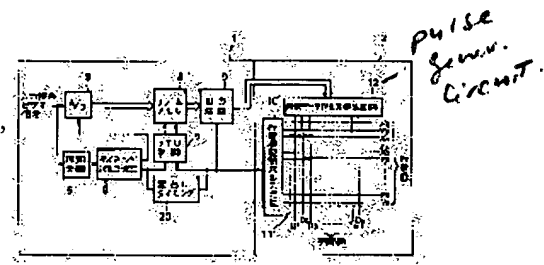
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(54) PLASMA DISPLAY PANEL OF AC DISCHARGE TYPE MATRIX SYSTEM AND METHOD FOR DRIVING IT

(57)Abstract:

PURPOSE: To drive a plasma display panel with a low power consumption by writing the pixel data while successively executing forced write discharge as preliminary discharge in order of arrangement of row electrode pairs.

CONSTITUTION: A row electrode drive pulse generation circuit 10' applies a positive polarity preliminary discharge pulse to a preliminary discharging row electrode Xp of the plasma display panel 11', and applies a negative polarity preliminary discharge pulse to the preliminary discharging row electrode Yp. Then, the row electrode drive pulse generation circuit 10' applies a positive polarity maintaining pulse to a row electrode X1, and applies a negative polarity forced write pulse to the row electrode Y1. Then, a pixel data pulse generation circuit 12 applies pixel data pulses corresponding to the pixel data of a first row to corresponding electrodes of column electrodes D1-Dn respectively. Together with that, the row electrode drive pulse generation circuit 10' applies the positive polarity maintenance pulse to the row electrode Y1. At this time, the forced write discharge by the forced write pulse is executed in order of the arrangement of the row electrode pairs successively.



LEGAL STATUS

[Date of request for examination] 08.09.2000

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

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